

said electronic monitoring circuitry includes a microprocessor adapted to measure the stored value and to output a digital value corresponding to the amplitude,

wherein the signal processing apparatus includes a third input to receive said digital value, said third input being adapted to feed said value to the signal processing circuitry.

81. The signal processing apparatus according to claim 80, wherein the second and third inputs comprise a single input which is adapted to receive and decode an information stream having information relating to both the triggering and the maximum amplitude.

IN THE ABSTRACT:

After the claims, please insert a page containing the Abstract Of The Disclosure, which is attached hereto as a separately typed page.

REMARKS

Original claims 1-43 and Amended claim 30 have been canceled and rewritten as new claims 44-81.

An Abstract Of The Disclosure has been added as a separately typed page to be inserted after the claims.

Examination and allowance of claims 45-81 are respectfully requested.

Respectfully submitted,

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